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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Complete if Known	
Application Number	10/608051
Filing Date	June 27, 2003
First Named Inventor	Glass, Kevin
Group Art Unit	Unknown 2816
Examiner Name	Unknown Linh Nguyen
Attorney Docket No: 80107.022US1	

US PATENT DOCUMENTS						
Examiner Initials*	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date if Appropriate

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS				T ³
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
lwn		CHAE, KWAN-YEOB, et al., "Double Precharge TSPC for High-Speed Dual Modulus Prescaler", <u>IEEE</u> , (1999), pp. 609-612		
lwn		CHANG, W-H, et al., "A Low-Power and Low-Noise CMOS Prescaler for 900 MHz to 1.9 GHz Wireless Applications", <u>IEEE 1999 Custom Integrated Circuits Conference</u> , (1999), pp. 597-600		
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lwn		DE MUER, BRAM, et al., "A CMOS Monolithic $\Delta \Sigma$ -Controlled Fractional-N Frequency Synthesizer for DCS-1800", <u>IEEE Journal of Solid-State Circuits</u> , Volume 37, (July 2002), pp. 835-844		
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lwn		KNAPP, HERBERT, et al., "36 GHz Dual-Modulus Prescaler in SiGe Bipolar Technology", <u>2002 IEEE Radio Frequency Integrated Circuits Symposium</u> , (2002), pp. 239-242		
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reference not available

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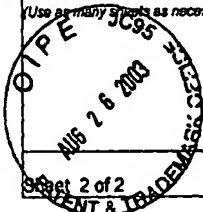
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OTHER DOCUMENTS – NON PATENT LITERATURE DOCUMENTS

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lnd		MALIGEORGOS, JAMES P., et al., "A Low-Voltage 5.1-5.8-GHz Image-Reject Receiver with Wide Dynamic Range", <u>IEEE Journal of Solid-State Circuits</u> , Volume 35, (December 2000), pp. 1917-1926	
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lnd		TREBOUT, MARC, "A 480µW 2GHz Ultra Low Power Dual-Modulus Prescaler in 0.25µm Standard CMOS", <u>IEEE International Symposium on Circuits and Systems</u> , (May 2000), pp. 741-744	
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lnd		YUN, WONJOO, et al., "New High Speed Dynamic D-Type Flip Flop For Prescaler", <u>ISIE 2001</u> , (2001), pp. 629-631	

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